

## **BIO DATA**

### **Personal Details**

Name : **V.KALAIYARASI**  
Designation : Assistant Professor  
Date of Joining : 01.06.2015  
Nature of Association : Regular  
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### **Academic Profile**

S.No	Qualification	Institution	Board / University	Full Time/ Part Time	Percent age / CGPA	Year of passing	Class Obtained
1.	M.E.- VLSI Design	Anna University Regional Centre, Coimbatore	Anna University	FT	8.49	2015	First class
2.	B.E.-ECE	Anna University of Technology, Tiruchirappalli (University College of Engineering, Tiruchirappalli)	Anna University	FT	8.42	2013	First class
3.	HSC	Gandhi Higher Secondary School, Kandhampalayam	State Board	FT	96.4%	2009	--
4.	SSLC	Govt. Girls Higher Secondary School, Tiruchengode	State Board	FT	95.4%	2007	--

### Employment details with Experience:

S.No.	ORGANIZATION	DESIGNATION	PERIOD		YEAR OF EXPERIENCE
			FROM	TO	
1.	K.S.Rangasamy College of Technology, Tiruchengode.	Assistant Professor	01.06.2015	Till Date (12.08.2019)	4.2
<b>Total</b>					4.2

### List of Publications

INTERNATIONAL JOURNALS	
1.	Mythili D, Rohini V, Nithya M, RinishaFarvin A and <b>Kalaiyarasi V</b> , “IoT Based Security Alert system for Smart Home”, Proceedings of 8 <sup>th</sup> International Journal of Science and Innovative Engineering & Technology, Issue , vol . 2, ISBN No. 978-81-923607-3-7, May 2018.
2.	Kanimozhi M, Kiruthikashree K, KousalyaK, Kesavamoorthi T, <b>Kalaiyaasi V</b> , “Digital data displaying board using GSM”, International Journal of Innovative Research in Computer and Communication Engineering Vol 5, Issue3, March 2017.
3.	<b>V. Kalaiyarasi</b> , K. Thiruvenkadam, J. Ramesh, “An area efficient multi-mode quadruple precision floating point adder”, Microprocessors and Microsystems (Elsevier), Volume 45, part B, Sep. 2016, Pages 310-323.

### List of Workshops / Seminar / STTP/FDPs Organized

Sl. No.	Title	Venue	Duration	Sponsoring Authority
<b>Workshop/Seminar</b>				
1	11 <sup>th</sup> National conference on “Emerging trends in Communication and Information Technology”	K S Rangasamy College of Technology, Tiruchengode	Feb 26 <sup>th</sup> 2019	--

2	Arduino based system design	K S Rangasamy College of Technology, Tiruchengode	01, September 2017	IEEE
3	Design and Development of Raspberry Pi based Embedded System	K S Rangasamy College of Technology, Tiruchengode	9 <sup>th</sup> Sep, 2017	IEEE
4	PCB Designing and Fabrication	K S Rangasamy College of Technology, Tiruchengode	16 <sup>th</sup> Sep, 2017	IEEE
5	Antenna Design and Simulation using ANSYS HFSS	K S Rangasamy College of Technology, Tiruchengode	6 <sup>th</sup> March, 2017	--

### **List of Workshops / Seminar / STTP/FDPs Attended**

Sl. No.	Title	Venue	Duration	Sponsoring Authority
<b>STTP/Online Course</b>				
1	CMOS Digital VLSI design	NPTEL Course	Feb – April, 2019	NPTEL SWAYAM
2	Enhancing Soft skills and Personality	NPTEL Course	Feb – March 2018	NPTEL SWAYAM
<b>Workshop/Seminar</b>				
1	Deep Learning: Smart Tool for Big Data Challenge in Healthcare	K S Rangasamy College of Technology, Tiruchengode	22-23, July, 2018	ICMR
2	Cognitive satellite geodesy image processing and remote sensing	K S Rangasamy College of Technology, Tiruchengode	23-24, June, 2018	MOES
3	Antenna Design and Simulation using ANSYS HFSS	K S Rangasamy College of Technology, Tiruchengode	6, March, 2017	--
<b>Faculty Development Programme</b>				
1	EC602-Principles of Digital Signal Processing	K S R Institute of Engineering and Technology, Tiruchengode	15 – 16 June 2017	--

**Kalaiyarasi V**