

BIO DATA

Personal Details

Name : K. SIVANANDAM
Designation : Assistant Professor
Date of Joining : 01.06.2011
Nature of Association : Regular
Email : Sivanandam@ksrct.ac.in



Academic Profile

| S.No | Qualification | Institution | Board / University | Full Time / Part Time | Percentage / CGPA | Year of passing | Class Obtained |
|-------------|----------------------|---|---------------------------|------------------------------|--------------------------|------------------------|-----------------------|
| 1 | Ph.D | K.S.Rangasamy College of Technology | Anna University, Chennai | Part Time | - | Pursuing | - |
| 2 | M.E | Govt. College of Technology, Coimbatore | Anna University, Chennai | Full Time | 70 | 2008 | First class |
| 3 | B.E | Govt. College of Engineering, Salem | Anna University, Chennai | Full Time | 67 | 2006 | |
| 4 | HSC | Govt. Hr. Sec. School, Paramathi | State Board | Full Time | 77 | 2001 | First class |
| 5 | SSLC | Govt. Hr. Sec. School, Paramathi | State Board | Full Time | 89 | 1999 | First class |

Employment details with Experience

| S.No | Organization | Designation | Period | | Year of Experience |
|--------------|-------------------------------------|---------------------|------------|------------|--------------------|
| | | | From | To | |
| 1 | K.S.Rangasamy College of Technology | Assistant Professor | 01.06.2011 | Till date | 8.2 |
| 2 | Paavai College of Engineering | Lecturer | 18.06.2008 | 20.04.2011 | 2.10 |
| Total | | | | | 11.0 |

List of Publications

International Journals

| | |
|----|---|
| 1. | K.Sivanandam, P.Kumar, “Design and performance analysis of reconfigurable modified Vedic multiplier with 3-1-1-2 compressor”, International Elsevier journal of Microprocessor and Microsystems, Vol.65, March 2019, Pages 97-106. DOI Link: https://doi.org/10.1016/j.micpro.2019.01.002 |
| 2. | K.Sivanandam, Muthuraman K, Prasanth G, Nanthini S, Anand S, “Smart Water Grid” International Journal of Advanced Research in Basic Engineering Sciences and Technology (IARBEST), Vol.4, Issue 5, May |
| 3. | K.Sivanandam, V.Nagaarjunan, S.Kathirvel, E.Kowsalya, R.Mathivanan, “Air Pollution and Insurance Based Vehicle Locking System”, International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering , Vol.6, Issue 3, March 2017, Pages 1693-1697. DOI Link: http://www.ijareeie.com/upload/2017/march/95_AIR.pdf |
| 4. | K. Sivanandam, S. Jayarajkumar , “High-Throughput 16-Bit Systolic Multiplier Using Modular Shifting Algorithm For NIST Pentanomials”, International Journal of Digital Communication and Networks (IJDCN), Vol.3, Issue 5, May 2016. DOI Link: ijdcn.co.in/download/2016/06/IJDCN_002V3ISSUE5.pdf |
| 5. | K.Sivanandam, P.Kumar, “Low-Power High-Performance Multitransform Architecture Using Run-Time Reconfigurable Adder for FPGA and ASIC Implementation”, Book series publisher- Springer/ Singapore. Print ISBN 978-981-10-8532-1 ; Online ISBN 978-981-10-8533-8 |

National & International Conference

| | |
|----|---|
| 1. | K.Sivanandam, Muthuraman K, Prasanth G,Nanthini S, Anand S, “Smart Water Grid”, 8 th International conference on engineering and advancement in technology -2018, 22-23 March 2018. |
| 2. | K. Sivanandam, S. Jayarajkumar, “Design and implementation of 16 bit systolic multiplier using modular shifting algorithm”, IEEE Published in: 2016 Second International Conference on Science Technology Engineering and Management (ICONSTEM), 30-31 March 2016. DOI Link: 10.1109/ICONSTEM.2016.7560950 Electronic ISBN: 978-1-5090-1706-5 |
| 3. | K. Sivanandam, Sridhar M, “Low adaptation delay in fixed point LMS adaptive filter for DSP applications”, IEEE Published in: 2016 International Conference on Wireless Communications, Signal Processing and Networking (WiSPNET), 23-25 March 2016. DOI Link: 10.1109/WiSPNET.2016.7566221 Electronic ISBN: 978-1-4673-9338-6 |
| 4. | K. Sivanandam, P. Kumar, “Run time reconfigurable modified Vedic multiplier for high speed multimedia applications”, 2nd International Conference on Computing for Sustainable Global Development (INDIACom), 11-13 March 2015. DOI Link: https://ieeexplore.ieee.org/document/7100611/ Electronic ISBN: 978-9-3805-4416-8, Print ISBN: 978-9-3805-4415-1 |

List of Workshops / Seminar / STTP/FDPs Organized

| S.No. | Title | Venue | Duration | Sponsoring Authority |
|-------|--|-------------------------------------|-------------------------------|----------------------|
| 1 | National Conference on “Emerging Trends in Communication and Information Technology (ETCIT-19)” | K.S.Rangasamy College of Technology | 26.02.2019 | - |
| 2 | National Conference on “Emerging Trends in Communication and Information Technology (ETCIT-18)” | K.S.Rangasamy College of Technology | 27.03.2016 | - |
| 3 | Two Days National Seminar on “Clinical Application of Emerging Sensor Technologies in Diabetes Management” | K.S.Rangasamy College of Technology | 04.10.2017 & 05.10.2017 | ICMR |

| | | | | |
|---|---|---|-------------------------------|------|
| 4 | National Conference on “Emerging Trends in Communication and Information Technology (ETCIT-17)” | K.S.Rangasamy College of Technology | 12.04.2017 | ICMR |
| 5 | National Seminar on “Smart Health Care Monitoring System using IoT” | K.S.Rangasamy College of Technology | 22.09.2016 & 23.09.2016 | ICMR |
| 6 | National Workshop on “FPGA Based VLSI Design Using XILINX ISE” | K.S.Rangasamy College of Technology | 01 day- 19.09.2015 | - |

List of Workshops / Seminar / STTB/FDPs Attended

| S. No. | Title | Venue | Duration | Sponsoring Authority |
|--------|---|---|----------------------------|----------------------|
| 1. | National Seminar on “Improving your API scores based on UGC regulations” | Department of Library, K.S.Rangasamy College of | 28.08.2017 | - |
| 2. | ISTE STTP on “CMOS, Mixed Signal and Radio frequency VLSI Design” | IIT Kharagpur | 30.01.2017- 04.02.2017 | MHRD |
| 3. | National seminar on “smart healthcare monitoring system using IoT” | Department of ECE, K.S.Rangasamy College of Technology | 22.09.2016 - 23.09.2016 | - |
| 4. | One day workshop on “Writing research papers, plagiarism and citation analysis” | Department of Library, K.S.Rangasamy College of Technology | 26.06.2015 | - |
| 5. | Faculty empowerment workshop on circuit theory and laboratory | K.S.Rangasamy College of Technology | 2.12.2014 - 6.12.2014 | EQIP Foundation |
| 6. | Attended training course for „Prime | Banglore | 22.05.2013- 24.05.2013 | Synopsys India |
| 7. | Attended training course for „Design Compiler 1-Synopsys” | Banglore | 08.05.2013- 10.05.2013 | Synopsys India |
| 8. | Participated in a „Mission 10X Workshop” | Paavai Engineering | 14.02.2011- 18.02.2011 | Wipro Technology |
| 9. | Attended „ICT Academy of Tamil Nadu” Sponsored workshop on | Anna University, Coimbatore | 27.01.2010- 29.01.2010 | - |
| 10. | Keil Embedded workshop on MC/ARM Technology | Anna University, Coimbatore | 09.10.2009 | - |

| | | | | |
|-----|---|---|------------|---|
| 11. | Attended NITTTR Sponsored “Faculty Development Program” on “Instructional Objectives” | Paavai Engineering College, Namakkal | 13.08.2008 | - |
|-----|---|---|------------|---|

Details of Membership In Professional Societies

| S. No. | Name of Professional | Grade of Membership | Membership No. |
|---------------|-----------------------------|----------------------------|-----------------------|
| 1 | ISTE | Life Member | Life Member: LM 85103 |