

BIO DATA

Personal Details

Name : **Dr. K SIVASUBRAMANIAN**
Designation : Associate Professor
Date of Joining : 01.06.2007
Nature of Association : Regular
Email : sivasubramanian@ksrct.ac.in



Academic Profile

S.No	Qualification	Institution	Board / University	Full Time/Part Time	Percentage / CGPA	Year of passing	Class Obtained
1.	Ph.D	Anna University	Anna University, Chennai	Part Time	=	March 2017	-
2.	M.E.	K.S.Rangasamy College of Technology, Tiruchengode	Anna University, Chennai	Full Time	77	May 2007	First Class with distinction
3.	B.E.	National Institute of Technology, Trichy	Bharathidasan University, Trichy	Part Time	58	Dec 2000	Second Class

Employment details with Experience:

S.No.	ORGANIZATION	DESIGNATION	PERIOD		YEAR OF EXPERIENCE
			FROM	TO	
1.	K.S.Rangasamy College of Technology, Tiruchengode.	Associate Professor	01.06.2015	Till date	4.3
2.		Assistant Professor	01.06.2010	31.05.2015	5.0
3.		Lecturer	01.06.2007	31.05.2010	3.0
Total					12.3

Awards Received

NIL

Funds Received

NIL

List of Publications

INTERNATIONAL JOURNALS	
01.	Sivasubramanian K & Jayanthi, K B 2015, 'Music-Inspired Harmony Search Algorithm for Fixed Outline Non-Slicing VLSI Floorplanning', World Academy of Science, Engineering and Technology, International Journal of Electrical, Computer, Energetic, Electronic and Communication Engineering, vol. 9, no. 6, pp. 482-489, ISSN : 2010-3778.
02.	Sivasubramanian K & Jayanthi, K B 2016, 'Voltage-Island based Floorplanning in VLSI for Area Minimization using Meta-heuristic Optimization Algorithm', International Journal of Applied Engineering Research, vol. 11, no. 5, pp. 3469-3477, ISSN : 0973-9769. (SCOPUS)
03.	Sivasubramanian K , Jayanthi, K B & Gunavathi, C 2016, 'Chip Area Minimization with Voltage-Island and Fixed-Outline Constraints Using Dual Level Meta-Heuristic Optimization Algorithms', Journal of Computational and Theoretical Nanoscience, vol. 13, no. 7, pp. 4427-4438, ISSN : 1546-1963. (SCOPUS)
04.	C.Gunavathi, K.Premalatha and K.Sivasubramanian , "A Survey on Feature Selection Methods in Microarray Gene Expression Data for Cancer Classification", Research Journal of Pharmacy and Technology, vol. 10, no. 5, pp. 1395 – 1401, 2017. (SCOPUS)
05.	Sivasubramanian , K, Haripriya R, Nandhini A, Nandhinidevi B & Ravichandiran B 2017, 'Modernized Irrigation System for Paddy field' International Journal of Innovative Research in Computer and Communication Engineering, vol. 5, Issue 3, pp. 5387-5391, ISSN(online) : 2320-9801, ISSN(Print) : 2320-9798.
06.	C.Gunavathi, K.Premalatha and K.Sivasubramanian , "Biomarker Selection from Gene Expression Data for Tumour Categorization Using Bat Algorithm", International Journal of Intelligent Engineering & Systems, vol. 10, no. 3, pp. 401 – 408, 2017. (SCOPUS)
07.	C.Gunavathi, K.Premalatha and K.Sivasubramanian , "An Application of Clonal Selection Algorithm in Gene Selection for Cancer Classification using Microarray Data", Research Journal of Pharmaceutical, Biological and Chemical Sciences, vol. 8, no. 1, pp. 173 – 182, 2017.
National & International Conference	
01.	K.Sivasubramanian , R.Nandakumar, "safety- Automation of Cars using Embedded Microcontrollers". National Conference on Computational Intelligence and Network Security, Vivekanandha College of Engineering for Women, Tiruchengode, 7th March, 2007.
02.	R.Praveena, K.Sivasubramanian , "Low Power Design Logic for High-Speed Floating Point Units". National Conference on Emerging Trends in Communication and Information Technology, K.S.Rangasamy College of Technology, Tiruchengode, 01 & 02 April' 09.
03.	R.Praveena, K.Sivasubramanian , "Leading Zero Counting Design Logic for High Speed Floating Point Units". National Conference on Communication and Signal Processing, Vivekanandha College of Engineering for Women, Tiruchengode, 7th April' 2009.

04.	S.Rajalakshmi, K.Sivasubramanian , “Peak and average power reduction using bit-swapping LFSR and scan chain ordering in scan based BIST”. National conference on system design and information processing, Excel engineering college for women/ECE, 18 Feb 2010.
05.	S.Kalaivani, K.Sivasubramanian , “Radix-2 modified Booth Algorithm based on a new VLSI architecture of parallel multiplier-Accumulator” National conference on Computing, Communications and Devices, Adithya Institute of Technology, Coimbatore, 25 th March 2011.
06.	M.Deepika, K.Sivasubramanian , “Design and Implementation of Layered LDPC Decoder” National conference on Emergencing Trends and Developments in Information, Communication, VLSI Design and Embedded systems, Adhiyamaan College of Engineering, Hosur, 21 st and 22 nd March 2012.
07.	M.Deepika, K.Sivasubramanian , “Design of Low power layered decoding architecture for low density parity check decoder” International Conference on Advances in Electrical and Electronics, Information, Communication and Bio-Informatics sponsored by IEEE, IETE and CSIR, Prathyusha Institute of Technology and Management, 24 th and 25 th January 2012.
08.	K.Supriya, K.Sivasubramanian , “Efficient Partial Parallel Decoder Architecture for Quasi Cyclic NB-LDPC Codes with Reduced Complexity” International Conference on Research and Development Prospects on Engineering and Technology(ICRDPET 2013), E.G.S Pillay Engineering College, Volume 3, 29 th and 30 th March-2013.
09.	Naveenprakash J, Sivasubramanian K , “Image Processing and Networking” International Conference on Emerging Trends in Science, Engineering, Business and Disaster Management (ICBDM 2014), Noorul Islam University, Volume 8, 28 th February-2014.
10.	Sivasubramanian, K & Jayanthi, K B 2016, ‘Improved Harmony Search Algorithm for Nonslicing Floorplanning of VLSI Chip with Fixed-outline Constraint’, in Proceedings of the IEEE Sponsored International Conference on Electronics and Communication Systems (ICECS 2016), Coimbatore, India, pp. 124-129
11.	Nepolean, M., and K. Sivasubramanian . ‘An optimized design for Fused add-multiply operation’, IEEE Sponsored International Conference on "In Green Engineering and Technologies (IC-GET 2016), pp. 1-5

List of Workshops / Seminar / STTP/FDPs Organized

Sl. No.	Title	Venue	Duration	Sponsoring Authority
Workshop/Seminar				
01	National Conference on Emerging Trends in Communication and Information Technology	K.S.Rangasamy College of Technology, Tiruchengode	2 nd and 3 rd March 2012	CSIR Sponsored
02	Two day workshop on Frontend and Backend ASIC Design using Synopsys EDA Tools	K.S.Rangasamy College of Technology, Tiruchengode	1 st &2 nd March 2013	KSRCT Sponsored
03	One day workshop onFPGA Based VLSI Design using XILINX ISE	K.S.Rangasamy College of Technology, Tiruchengode	19 th September 2015	KSRCT Sponsored

04	Two day seminar on Cognitive Satellite Geodesy Image Processing and Remote Sensing	K.S.Rangasamy College of Technology, Tiruchengode	23 rd & 24 th June 2017	Ministry of Earth Science Sponsored
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List of Workshops / Seminar / STTB/FDPs Attended

Sl. No.	Title	Venue	Duration	Sponsoring Authority
Workshop/Seminar				
01	Workshop on Automotive Electronics	K.S.Rangasamy College of Technology, Tiruchengode	22-09-06 to 23-09-06	
02	Workshop on Circuit Simulation using Multisim Software	K.S.Rangasamy College of Technology, Tiruchengode	08-12-07	
03	Workshop on Design Verification Methodologies	VLSI Society of India, Bangalore	25-04-08 to 26-04-08	
04	Workshop on VLSI Design flow and FACE Programme	Reva Institute of Technology and Management, Bangalore.	23-06-08 To 30-06-08	
05	Workshop on Tanner Tools pro	K.S.Rangasamy College of Technology, Tiruchengode	09-08-08	
06	Workshop on Embedded System Design - A Systematic Approach	Sri Sivasubramaniya Nadar College of Engineering, Chennai.	18-09-08 to 20-09-08	
07	Seminar on Emerging trends in embedded controller and its applications	K.S.Rangasamy College of Technology, Tiruchengode	14-11-08	
08	Workshop on Graphical system design in engineering education	K.S.Rangasamy College of Technology, Tiruchengode	26-02-09	
09	One day national level seminar on advances in wireless communication.	K.S.Rangasamy College of Technology, Tiruchengode	05-03-10	
10	Workshop on high impact teaching skills(Mission10X provided by WIPRO)	VSB Engineering college, Karur.	17/05/2010 to 21/05/2010	
11	SDP on EDA Tools for VLSI design and signal processing	SSN College of Engineering, Chennai.	15/11/10 to 18/11/10	

12	Workshop on Xilinx FPGA based signal processing Applications & wind River Vx Works	K.S.Rangasamy College of Technology, Tiruchengode	17/3/11 to 19/3/11	
13	Workshop on system Integration challenges and solutions for mixed signal design	Thiagarajar College of Engineering, Madurai	4/7/11 to 5/7/11	
14	LABVIEW CORE –I &II	National Instruments	14/11/11 to 18/11/11	
15	IC Compiler-1	SYNOPSYS	29/03/12	
16	Workshop on microprocessor and microcontroller system design	Thiagarajar College of Engineering, Madurai	12/03/2014 To 14/03/2014	
17	Faculty empowerment workshop on VLSI design and laboratory	EQIP foundation-educational quality improvement programme	09/12/14 To 13/12/14	
18	Workshop on linear algebra for electrical sciences	Jagadguru Dr. sri. Shivarathri Rajendramahaswamiji Sri Jayachamarajendra College of Engineering, Mysuru	26/02/16 To 29/02/16	
19	Two week ISTE STTP on CMOS mixed signal and radio frequency VLSI design	National mission on education through ICT-MHRD – Indian Institute of Technology, Kharagpur	30/01/2017 To 04/02/2017	
20	ICMR Sponsored National Seminar on Deep Learning-Smart Tool for Big Data Challenge in Health Care	K.S.Rangasamy College of Technology, Tiruchengode	22/07/17 to 23/07/17	
21	One day Seminar on How to write a successful project proposal to funding agencies	K.S.Rangasamy College of Technology, Tiruchengode	21/07/18	
Faculty Development Programme				
01	Recent Trends in Process Automation	Pondicherry Engineering College, Pondicherry	30-04-07 to 13-05-07	
02	Signals and Systems	Dr.Mahalingam College of Engineering & Technology, Pollachi	10-12-07 to 22-12-07	
03	Intelligent Transportation System	Centre for Development of Advanced Computing	27.05.2013 to 05.06.2013	

Details of Membership In Professional Societies

S. No.	Name of Professional Body	Grade of Membership	Membership No.
1	IETE	Life Member	AM 217758
2	ISTE	Life Member	LM 54047

Dr.K.SIVASUBRAMANIAN